

A Critical Review of Cu_2SnS_3 (CTS) Thin Films Solar Cells

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Abstract— To increase energy demand, reliability, and increasing efficiency, thin-film solar cells get the main focus. Various types of solar cell like $\text{Cu}(\text{In,Ga})\text{Se}_2$ (CIGS), Cadmium telluride (CdTe) and copper zinc tin sulfide (CZTS) based absorber compound now eliminated by CTS (Cu_2SnS_3) for different reason like toxicity, shortage and structural complexity. Focus on CTS is increasing because it is nontoxic and it is environmentally friendly. CTS-based solar cell's power conversation efficiency increases and 30% theoretical efficiency indicates it's developing sign. For analyzing the synthesis property, X-ray diffraction (XRD), Raman, Energy Dispersive X-ray (EDX), X-ray fluorescence (XRF) techniques are used. For the synthesis of thin-film solar cells like CTS, different physical methods are used among all sputtering methods. The electron beam evaporation vacuum evaporation method has exposed better efficiency and high product quality and reliability. Additionally, the composition, variation of thickness, structural defects, and elemental composition make the absorber layer's quality affect the performance. This paper has discussed the annealing treatment of CTS-based solar cell synthesis, and it's essential to improve the thin film properties of thin films. This paper describes the reason for efficiency reduction also the scope for future research.

Keywords— CTS, Thin Film, Solar Energy, Thin-film solar cell, Sputtering.

I. INTRODUCTION

To meet the increasing energy demand, solar energy is the most promising solution to meet present and future energy demand. For increasing efficiency and decreasing cost solar cell is now in trading. It is a semiconductor material that's initiate electrical energy [1]. Now photovoltaic-based solar cells produce one percent of words energy production [2]. Now solar cells based on crystalline silicon reach power conversion efficiency PCE 26.7% [3] though thin-film is in a remarkable position because it uses less raw material. Within thin-film solar cells, PCE is 22.1% and 22.6% for CdTe and CIGS-based solar cells, including low-temperature coefficient [3, 4]. CIGS production depends on a rare element like Se and Ga. Due to lacking the raw

material it can't use for hug use also price can increase suddenly [5]. For Cadmium telluride (CdTe), Cd is more toxic, and beside Tellurium is comparatively rare. So the researcher focused on the environment-friendly solar cell. For this, CTZS is an example. Its material and property of bandgap is the same as CTS. However, the power conversion efficiency is significantly less than CdTe and CIGS [2]. CTS absorber layer is environmentally friendly; beside that it is low cost and it's raw material are available. CTS is a P-type semiconductor that's contains high coefficient absorption coefficient of about 10^4 cm^{-1} [5], and also it has a tunable bandgap (1.1-1.5eV) that is effective for photovoltaic absorption [2]. The technological status of CTS thin films are shown in Table I.

The current highest PCE of CTS is 4 to 6%. Theoretical efficiency is about 30%, which is quite high [2]. Hence the

TABLE I
TECHNOLOGICAL STATUS OF CTS THIN FILMS

Process	Authors	Deposition Method	η (%)	Voc (mV)	Jsc (mA/cm ²)	FF (%)
Vacuum	[6]	Sputtering	3.05	243	26.2	47.9
	[7]	Sputtering	0.76	215	13.8	25.5
	[8]	EBE	1.20	134	27.9	31.2
	[9]	EBE	2.70	244	29.0	38.5
	[10]	EBE	2.54	211	28.0	43.0
	[11]	Evaporation	3.66	248	33.5	43.9
	[12]	Evaporation	4.29	258	35.6	46.7
	[13]	Evaporation	4.63	283	37.3	43.9
	[14]	PLD	0.82	260	11.90	24.0
Non-vacuum	[15]	ED	2.84	249	29.3	39.0
	[16]	ED	0.54	104	17.1	30.4
	[17]	Ball milling	1.92	320	23.4	28.0

best efficiency of Na doping on CTS was found 4.6%[12]. Moreover, Umehara et al. reported that Ge-induced CTS had achieved the highest PCE, 6.7%, with changing bandgap structure using the co-sputtering method[18]. Physical and chemical both method of synthesis is applicable for CTS based solar cell. Including physical method sputtering [2], E-beam evaporation [8], Coevaporation [19], ball mill [17] etc. For chemical method electrodeposition [16], SILAR (successive ionic layer adsorption and reaction) [20], spray pyrolysis [21], etc. are most common. Physical methods are costly and more complex, but its final product is pure so use of physical method is more perfect [2, 5]. However, the chemical methods are cheaper and very little material waste, so the chemical method is also better for production in another logic. So it is clear that both methods have benefits depends on the expected outcome. Annealing treatment processes has a vital role in increasing efficiency [2]. PCE, Open circuit voltage(Voc), short circuit current density (Jsc), fill factor(FF) of CTS thin-film solar cell depends on bandgap, crystal structure, annealing temperature along with synthesis process [3, 22]. This study focuses on the PCE of CTS by analyzing different physical and chemical methods with various parameters.

II. CTS PROPERTIES

A. Crystal structure

The CTS has various phases like cubic, tetragonal, orthorhombic, monoclinic, and triclinic, which were reported [23-26]. The varying degrees of disorder on the cation sublattice were representative of the different crystal structures recommended by [26], as shown in Fig I. The various phase variations are introduced from the octet rules, ensuring the lowest energy level of a stable compound.

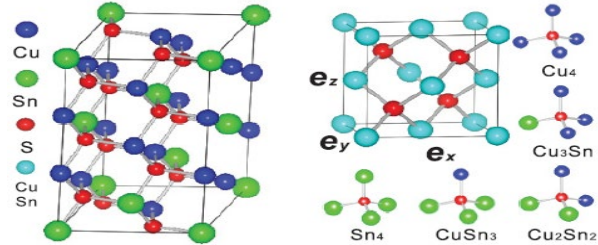


Fig I: CTS crystal structure a) monoclinic structure b) cubic structure and Cu₄, Cu₃Sn, Cu₂Sn₂, CuSn₃ or Sn₄ Structure.

CTS's five tetrahedral coordination is possible as an anion such as Cu, Cu₃Sn, Cu₂Sn₂, CuSn₃, and Sn₄. However, those were unsatisfied with the octet rule[26], which shows that the charge-neutral CTS compound, the anion atoms with 1:2 ratio can exist surrounding Cu₂Sn₂ and Cu₃Sn clusters. The formation of different CTS phases results from a long-range disorder of anion, while the short-range disorder anion well defined stable compound. The zinc blende structure resulting from full anion disorder results in a cubic unit cell, a zinc blende structure with a partial disorder created tetragonal structure, and Zinc blende structure with full disorder created monoclinic structure. Each of the phases had a structural and associated composition variation expected to induce different splitting mechanisms and wave-orbital overlapping. As a result, a wide range of varying in the concomitant optoelectronic properties and electronic band structure include absorption coefficient, electron affinity, and bandgap [23, 24, 27]. The preferential texturing estimation for cubic phase CTS are shown in Table II. Zhai et al. was analyzed that CTS exists only cubic, monoclinic, and tetragonal structures[26].

TABLE II
PREFERENTIAL TEXTURING ESTIMATION FOR CUBIC PHASE CTS

Plane (h k l)	Reference value	Experimental value	Reference value	Experimental value
(111)	28.4467	28.4435	100	100
(200)	32.9638	32.9654	13.61	6.73
(220)	47.3104	47.2854	47.75	45.56
(311)	56.1314	56.0780	29.73	11.68
(311)	76.3896	76.3553	8.70	3.04
(422)	88.0470	88.0130	8.81	1.71

The XRD has mainly been utilized to determine the product's phase purity like CTS's structural characterization. The Fernandes et al. showed that the tetragonal structure had the ratio c/a close to 2 ($a = 5.41\text{\AA}$ and $c = 10.81\text{\AA}$) whereas cubic CTS had a lattice parameter of $a = 5.43\text{\AA}$ [23]. In this study also provide detail comparison cubic and tetragonal structure of preferential texturing of data, which are shown in Table III.

TABLE III
PREFERENTIAL TEXTURING ESTIMATION FOR TETRAGONAL PHASE CTS

Plane(h k l)	Reference value	Experimental value	Reference value	Experimental Value
(112)	28.5398	28.5656	100	100
(200)	33.0703	33.1398	13.21	4.75
(204)	47.4725	47.5070	45.95	44.17
(312)	56.3246	56.3369	27.73	12.34
(316)	76.6807	76.8577	7.61	1.74

The ICDD reference number for cubic, tetragonal, and monoclinic were claimed as 01-089-2877, 01-089-4714, and 01-070-6 338, respectively, elsewhere space group of this phases were marked as F-43m, I-42m, and C1c1, respectively [7, 23]. Using XRD analysis, the sample were assigned as the plane of (1 1 2), and (-1 3 1), respectively where as the dominant peak positions for the cubic, tetragonal and monoclinic were found as 28.45 0, 28.58 0, and 28.50 0, respectively [23]. Although CTS were also found the other characteristics peaks at 33.5⁰, 47.5⁰, and 56⁰ at the 2 θ position [11, 12, 19, 23, 28, 29].

The CTS structure major peaks like cubic, tetragonal, and monoclinic are originated at very near to 2 θ values, it's made difficult to the structural phase differentiate. Secondly, the same CTS cubic structure can exist in the Cu₃SnS₄ impurity phase and detecting the lattice parameters is sufficient. Therefore, the XRD has the detection limit in the range within 1-5%, and the effect of the optoelectronic properties of the material could include impurity phases in lower concentrations.

Raman study was used as a supplement that XRD analysis alone cannot identify the phase ambiguity of CTS thin film as well as those in recent studies published in the last several years. The Raman spectra when used to identify the crystal structure of CTS then its peak don't overlap like as XRD peaks. Therefore, CTS or cu-Sn-S phases have no theoretical work to calculate the Ramon active modes. Though, there is no disagreement in the field about the exact peak position of different phases because phase assignment is empirical. Raman's studies analyzed cubic, tetragonal and monoclinic phases that are listed in Table IV. almost all phases have Raman mode near the shift of 350 cm⁻¹, source

of data shown in Table IV, and the analogy of CZTS is assigned as A₂ mode [30, 31].

Moreover, the different phases are the varied prime mode of Raman study. For the CTS, tetragonal and monoclinic is the found value 336 and 290 cm⁻¹, as well as for the cubic CTS active Raman mode (A₁) is located at 303 cm⁻¹. Although, Ramon's study is important to point out the CTS phase for the different active Raman modes (A₁), in the distinct phases. Moreover, by combining Raman and XRD have been achieved for accurate phase identification. Recently, for the characterization, another study has been used X-ray photoelectron spectroscopy (XPS) to the oxidation state of each element. This data can help verify phase purity [14].

B. Compositional and morphological properties

The most important parameter of CTS thin film is controlling by the Cu/Sn ratio. The prepared CTS can be either Cu-poor state (Cu/Sn<2), Cu-rich state (Cu/Sn>2), or stoichiometric state (Cu/Sn=0) using based on Cu/Sn = 0.

TABLE IV
REPORTED RAMAN STUDY DATA FOR DIFFERENT CTS PHASES

Raman Shift (cm ⁻¹)	Assigned Phase	Sources
303,355	Cubic	[23]
303,355	Cubic	[20]
335,351	Tetragonal	[32]
336,337,351	Tetragonal	[23]
297,337,352	Tetragonal	[33]
295,337,354	Tetragonal	[34]
285,332	Tetragonal	[19]
290,352,314 (weak), 374 (weak)	Monoclinic	[6]
290-295,354-357,314 (weak), 374 (weak)	Monoclinic	[35]
292,349,255 (weak)	Monoclinic	[11]
290,352	Monoclinic	[13, 36]

Although, in terms of sulphur (S) content, CTS thin film can be again divided as S-rich state (S/metal>1) and S- poor state (S/metal<1). Zhang et al. was stated that CTS thin-film solar cell is not able to getting high efficiency in Cu-rich state [37]. The stoichiometric state of CTS has found the same scenario in the absorber layer. In contrast, Cu-poor state, Cu-rich state were obtained the comparatively most high efficiency (U-2013, N-2015, K-2015). The high efficiency was attributed to formation of Cu-vacancy (V_{cu}) as a result of lack of Cu atoms that is actually an acceptor type of defect as well as physical explanation.

C. Optical properties

Multiple theoretical works have calculated CTS thin films' bandgap in the range of 0.84-0.88 eV [26, 27]. However, the experimental results revealed that band gap of CTS could vary from 0.83-0.177eV [17, 38]; nevertheless, most often, this value has been found constant in the range of 0.86-1.02 eV, which was similar to the reported theoretical value [10,28,34,39,40]. Zhang et al. (2014) reported a route to produce highly crystalline CTS thin films through sulphurization of sequentially sputtered Sn -rich precursors. Cu/Sn strucked precursors were preheated at 200 °C for 4hr under vacuum (10-03 Pa) followed by sulphurization of samples at 560 °C for 2 hr. cubic CTS phase was formed with a bandgap of 1.35 eV [37]. Dong et al. (2015) fabricated CTS thin films with sulphurization temperature variation from 400 to 500 °C. They observed the effect of elevated sulphurization temperature in terms of crystallinity, chemical composition, and optical bandgap. The higher sulphurization temperature can be attributed to better crystallinity and proper chemical composition of CTS thin films. The optical band gap was measured as 0.92 eV, 0.96eV, 0.98 eV and 0.99eV for the sulphurization temperature of 400 °C, 450 °C, 500 °C, 550 °C etc [39].

Similar to temperature, sulphurization time affects the optical properties of CTS thin films. Dong et al. (2015) reported the correlations of sulphurization time and CTS thin-film S properties. They have deposited metallic precursors with sulphurization for 5 to 30 min at 575 °C in a nitrogen environment. The cubic phase was identified for each sulphurized sample having ICDD card no 01-089-2877 [39]. However, the change bandgap was measured as 1.02 eV, 1.01 eV, 1.00eV, and 0.98 eV, for the sulphurization time for 5min, 10min, and 30min, respectively.

D. Electrical properties

CTS's probable antisite defects are Cu_{zn} , Zn_{cu} , and the defects states V_{cu} , V_{zn} , V_s as vacancy defects. Hence CTS works as a p-types semiconductor, whereas the acceptor type of defect V_{cu} is the lowest defect [41]. The Cu atom deficiency indicates that Cu poor is an important condition to fabricate CTS solar cells successfully.

The electrical properties of CTS is varied in different literature, which was reported significantly. The resistivity of CTS material was measured at any point between 0.0004 to 10 Ω -cm [23, 34]. Small amounts of conductive material phases such as Cu_4SnS_4 and Cu_2S incorporated impurities during various processing steps, and the different synthesis conditions vary the intrinsic defect concentration. Therefore, The CTS is always reported as a p-type semiconductor in the absorber layer, and the carrier concentration ranging from 10^{17} to 10^{21} $1/cm^3$ [6, 24]. However, the value of carrier concentration is very high related to photovoltaic absorber materials. The majority carrier mobility characterization has to get value from 0.03-22.3 cm^2/Vs using Hall Effect measurement [28, 33]. Therefore, there does not seem to be an important correlation between the grain size (using SEM) and the majority carrier mobility because the means free path is limited by defects within the grain [20].

The conduction mechanism of CTS has been made to understand to need several steps. The electrical conductivity has been measured between the room temperature to 85K as

well as observed to different conduction regimes [42]. According to Arrhenius's law, the relationship of the electrical conductivity and temperature can be fit above 238K, and is pointed to thermal excitation charge carriers. According to Mott's law, the data can be fit below 238K, indicating the variable range hopping mechanism [43]. Similarly, Dias et al. (2014) reported that the transition temperature is 275K. Differently, Tiwari et al. observed three types of conduction regimes: Mott variable range hopping from 5-50K transport is dominated, nearest neighbour hopping from 50-150 K, and thermionic emission over grain boundaries from 150-290K [44]. Therefore, Tiwari et al. have a second work analyzed that in terms of oxygen defects, the change in transport behaviour in their film results from annealing in the air [45].

E. Basic structure of CTS TFSC

The schematic structure of the CTS solar cell is shown in Fig. II. For the deposition of a CTS solar cell, soda-lime glass (SLG) is used as a substrate. Molybdenum (Mo) thin film with a thickness of 1.2 to 1.5 μm is sputtered on a glass substrate as back contact because Mo is stable in harsh reactive conditions such as sulphur-containing vapour and high temperature. The absorber layer of p-types CTS thin-film coated Mo thin film thickness from 1.0 to 2.5 μm .

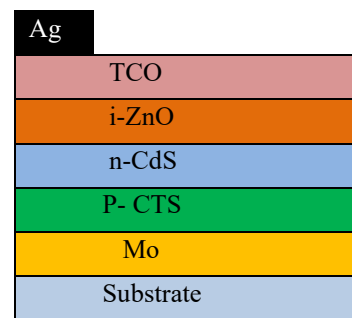


Fig. II Schematic diagram of a typical CTS Solar cell

III CTS FILM DEPOSITION TECHNIQUE

Fuku and Fakoluju have successfully fabricated the first CTS thin-film solar cell in 1987 using various techniques [38]. The two major methods used for the fabrication of CTS thin-film solar cells are the Vacuum deposition technique and Non - vacuum deposition technique.

A. Vacuum deposition technique

The vacuum deposition is a one-of-the-way fabrication that has a semiconductive batter process and large grain size. The two most important vacuum-based methods are evaporation and sputtering, notable for fabrication for the efficient CTS thin-film solar cell. However, the pulse laser deposition process (PLD) is also a vacuum base deposition.

1) Evaporation

Evaporation is one of the established vacuum thin-film deposition methods. The CTS thin film was fabricated first time by using a direct evaporation technique[38]. The efficiency 0.11% was reported using in the schottky -type solar cell, where short-circuit current 0.3mA, the open-

circuit voltage 170 mV, and a fill factor of 0.48. Using electron beam deposition technique by Chino et al. at was fabricated CTS thin-film solar cell [10]. They used the deposition of elemental Cu and Sn sequentially as a precursor. After the deposited film, they were annealed in the S and nitrogen (N) atmosphere for 2 hr at different temperatures from 450 °C to 580 °C to determine the optimum annealing temperature. CTS solar cell's best efficiency was getting 2.70%, and the produced a large grain size at 550 °C, leading to a decrease in grain boundaries. The effect of the CTS solar cell performance was deposited on the Cu/Sn ratio, using electron beam deposition technique [9]. This study was deposited a wide range of sample varied from 1.39 to 2.22 and sulphurized them at 560 °C for 2 hr. The most efficient cell was found for the deposition Cu/Sn ratio of 1.62, and the ratio of Cu/Sn sulphurized samples had been found in the range 1.62 to 2.44, with the PCE of 2.93%.

In the following year, Aihara et al. reported a work using the same vacuum deposition technique (2014). Therefore, this study optimized the sulphurization temperature from 500 °C – 580 °C with a 20 °C step. This study took the deposited ratio Cu/Sn 1.7, and the ratio changed after sulphurization from 1.79 to 1.89 for the variation of temperature from 500 °C to 580 °C. The CTS solar cell was found efficiency 2.7% of 0.15 cm² for the sulphurization temperature of 580 °C [40]. Table V summarizes of CTS finding for sputtering deposition technique.

The fabrication of CTS thin-film solar cell was reported using the co-evaporation method [11]. Therefore, they optimized the sulphurization temperature and optimized Cu/Sn ratio too. The deposited Cu/Sn ratio was taking 1.7 to 1.8, and the sulphurized temperatures from 520 °C and 580 °C for 5 min per sample. The best quality of CTS found in terms of efficiency 3.66% at the 570 °C temperature with sulphurized ratio Cu/Sn of 1.8 from the area 0.154 cm² with the value of Voc, Jsc, and FF were 248mV, 33.5mA/cm² and 0.44, respectively. After that, the work has also optimized the ratio of Cu/Sn. This study varied the deposited ratio 1.80 to 1.98 within the narrow reason and sulphurized them at the efficiency of non-alloyed, non-doped pure CTS thin film was getting 4.29% that is for the Cu/Sn ratio of 1.89. Another group worked in 2015 on CTS thin film using the co-evaporation deposition technique [19, 29, 34, 36]. Moreover, in 2015 Nakashima et al. successfully incorporated Na with the CTS thin-film solar cell and performed a comparison study without Na-doping effect on CTS thin-film solar cell. This study was found that the significant influence regarding CTS thin film and CTS was getting the value of Voc = 283 mV, FF=0.439mA/cm², Jsc= 38.3 mA/cm² and η= 4.63% in the mole ratio NaF/Cu=0.075[13].

2) Sputtering

The sputtering method is one of the best suitable and most controllable techniques for comparative all vacuum-based techniques that might be applied in the thin film production at the commercial level. The first reported CTS thin film using sputtering in the year 2010 by Fernandes et al.[23]. In this study, the sample was sequentially deposited the element Cu and Sn target using dc magnetron deposition during 10 min, whereas the power density of 0.11 Wcm⁻² and

0.11 Wcm⁻² respectively. The deposition was carried out working pressure of 6*10⁻⁶ mbar and 2*10⁻³ mbar, respectively, and Ar atmosphere with a base. The samples were sulphurized at the temperatures 350 °C, 400 °C, and 520 °C in the (S₂ + N₂) atmosphere during 10 min for the sample deposition. In this work, the CTS thin-film device was exhibited in phases like Cubic, Tetragonal, and orthorhombic, identified by XRD and Raman analysis. The orthorhombic phases have the atomic structure Cu₃SnS₄, and the cubic phase has the atomic structure Cu₂SnS₃.

The first CTS solar cell was fabricated using the sputtering technique by Umehara et al. [46]. In this study, they were co sputtered in an argon atmosphere, including working power of 0.5 Pa and using Cu and Sn dc gun at RT. The sulphurized in two different ways of a condition like as at normal (S₂+N₂) atmosphere and with the presence of GeS₂ where the effect of Germanium (Ge) allows in CTS's performance alloying. They got the bandgap of 1.02 eV for the CTS alloy with Ge in a cubic phase that showed the great efficiency of 6.01 % in the ratio of 0.17/0.83 of Ge/Sn ratio, non-alloyed solar cell showed only 2.13%.

In the year 2014, analyzed the optimization of the precursor ratio Cu/Sn using the dc sputtering method. The pressure and power were set to 0.8 Pa and 50 W, respectively, and at the room temperature sequentially to deposited Cu and Sn. The time for Cu deposition was setup during 30 min and the time for Sn layer was to set up 10 to 30 min, whereas the deposited Cu/Sn ratio from 2.7 to 0.86. The Sn rich samples (Cu/Sn<2) showed at 560 °C for 2 hr after sulphurized the sample whereas the stoichiometric value (Cu/Sn=2) a self- adjustment tend of the ratio Cu/Sn.

The author mentioned that the metallic precursor was sulphurized at high temperature (>550 °C) during 1hr to produce SnS by re-evaporation of Sn samples the stoichiometric value of Cu/Sn ratio. Although, At first sulphide of both copper and tin was formed during the sulphurization of metallic precursors. The ternary Cu₂SnS₃ is formed and these binary sulphides combine together as well as the sulphurization time increase. The extra Sn portion is evaporated as SnS for Sn-rich precursors. The CTS sulphurized sample in the Cu/Sn ratio is the precursor stoichiometric value for Sn-rich. Therefore, The sample had pinhole on the film surface for the evaporation of SnS caused crack, that selecting the deposition Cu/Sn ratio should be controlled, which should be around suggested by this work.

Three sequential studies were reported regarding the sputtering deposition of CTS thin-film solar cell by Dong et al. [7, 28, 39]. The elemental Cu and Sn were sequentially deposited at room temperature for all studies whereas used RF sputtering power density of 4 Wcm⁻² and 2 Wcm⁻², respectively.

In this study, they optimised the sulphurization temperature from the ranges of 400 °C to 500 °C. Hence, the best performance was exhibited the sulphurization temperature at 550 °C for 20 min, having a direct band gap of 0.99 eV for the CTS cubic phase. This means that the higher sulphurization temperature is beneficial for the fabrication of CTS thin film and the sample showed high efficiency as 0.28%.

TABLE V

SUMMARIZATION OF CTS FOR SDT

Ref.	Deposition	Sulphurization	η (%)	Voc (mV)	Jsc (mA/cm ²)	FF (%)	Surface Morph.
[23]	2*10 ⁻³ mbar sequential	350 ^o C, 400 ^o C, 520 ^o C, 10min	-	-	-	-	Rough surface with small grain size
[46]	0.5 Pa co-sputtering	GeS ₂ S ₅ 50 ^o C, 15 min	35.5	30.5	55.6	6.01	Large grain size above micrometer
[37]	50W, 0.8Pa sequential	560 ^o C, 2 hr	-	-	-	-	Smooth and compact surface
[28]	4,2 Wcm ⁻² , sequential	400 ^o C to 550 ^o C 20min	18.0	5.45	26.1	0.28	grain size increased with temperature
[39]	4,2 Wcm ⁻² , sequential	575 ^o C 5 to 30 min	19.7	3.60	26.1	0.20	Dense surface was observed
[7]	4,2 Wcm ⁻² , sequential	570 ^o C 20min	21.5	13.8	25.5	0.76	Cracks and pores decrease with preheating duration
[6]	Empirical co-sputtering	520 ^o C, 560 ^o C 1 hr	24.3	26.2	47.9	3.05	Large gain size and compact

The second study optimized the sulphurization duration, where the previous study was the optimization of the sulphurization temperature [28]. This study was varied the duration from 5 min to 30 min by taking sulphurization temperature at 575 °C. The optimized duration 20 min was getting the efficiency of 0.2 % and FF, Voc, Jsc of 0.26, 197 mV, 3.60 mA/cm², respectively. The third study investigated a two-step sulphurization method for the sulphurization pre heating time [7]. Hence, this study was followed the two step sequential sulphurization step for the deposition of sample. Firstly, the sample during the time 5 min to 30 min preheated at 250 °C in (S₂+N₂) atmosphere after that annealed them for 20 min as well as temperature raised up to 570 °C.

The CTS fabrication film was showed excellent optical properties and good morphology for preheating 15 min duration. Finally, from these three studies, they achieved efficiency 0.76% and FF, Voc and J_{sc} of 0.26, 215 mV, 13.8 mA/cm² respectively for the CTS solar cell.

Moreover, another study CuS and SnS target was selected as a precursor using co-sputtering by Chierchia et al. [6]. They sulphurized for the deposition at 520 °C, and 560 °C during 1 hr in the (S₂+N₂) atmosphere showed the monochromatic structure and two Ramon shift at 290 cm⁻¹ and 352 cm⁻¹ respectively. Hence, the sample was exhibited large grain size with better morphology beyond the micrometer range where the ratio Cu/Sn of 1.84 at the sulphurized temperature at 520 °C. The fabricated CTS thin-film solar cell device achieved an efficiency as good as 3.05%, which is the better value for non-doped, non-alloyed CTS solar cells by a sputtering deposition method.

3) Pulse lased Deposition (PLD)

The deposition of CTS thin-film solar cell using PLD technique was used the first time [47]. This study tried to explore the influence of substrate temperature on the deposition rate of CTS and optical properties as well as the electrical properties.

Similarly, Vanalakar et al. was also fabricated CTS thin film using the same PLD technique. This study mixing with SnS₂ and Cu₂S powder by 1/1 molar ratio that was used for the deposition source as well as for the CTS synthesized. The annealing temperatures were at 200 °C, 300 °C, and 400 °C for 1 hr using for the deposition precursor film. The average grain size films were varied from 80 nm to 400 nm, and the annealing temperature was varied 200 °C to 400 °C for the CTS polycrystalline cubic phase. The fabricated device was getting higher efficiency of 0.82% at the annealing temperature of 400 °C, with a bandgap of 1.01 eV [14].

B. Non – vacuum deposition technique

The various issues have been suffering in the non-vacuum deposition, which is the barrier to getting a higher efficiency solar cell. Hence, the vacuum methods are costly than the non-vacuum method synthesis processes, which is commercially important.

1) Ball Milling

Ball milling is one of the most commonly used methods for mechanical alloying deposition. It is a simple and economical physical technique as the deposit is carried out in a non-vacuum environment.

Chen et al. (2012) fabricated CTS nanoparticles in a lower milling time of 1 hr where they increased the milling rotation speed up to 800 rpm. The as-deposited samples were annealed in the different temperature range from 350 °C to 500 °C. However, the samples annealed at 400 °C exhibited the highest efficiency of 1.10 %. This team also worked on the modification of p-n junction interface. This term also worked on the improvement of p-n junction of the p-n junction interface. Using In₂S₃ as the n-type buffer layer, the efficiency was further increased up to 1.92%. To obtain better carrier collection, the p-n junction should be intimate,

smooth, and uniform to ensure the free flow of electrons and holes[17].

High-pressure sintering is employed to enhance the crystallinity and achieve a desirable compact microstructure. The application of pressure during the annealing step (5- 50 pa) compresses the grains leading to their merging with other grain, resulting in compact and void-free microstructure [33, 37]. Nomura et al. (2014) synthesized CTS thin films by ball milling followed by post-annealing treatments with high-pressure sintering at 6 MPa followed by subsequent sulfurization. The Cu/Sn ratio 1.9 and post-annealing treatment at 600 °C showed 1.3% efficiency[48]. Neves et al. (2016) succeeded in improving the morphology by using spark plasma sintering (SPS). The sintering was done at 600 °C for 5 min at 50 Mpa, which ensured pure CTS formation. The morphology was found as compact, uniformed, and highly crystalline, which is favourable for solar cell application [49].

2) Solid-State Reaction

In the solid-state reaction method, the predecessors are mixed and reduce the particle size by ball milling to develop an extreme surface area to accelerate the reaction rate. Actually, it is the long-established procedure of fabrication to getting the desire compound. This reaction is a temperature-dependent reaction. To complete this reaction temperature range is 500-1200°C, and it takes two to fifteen days(2- 15days) to complete. The maximum number of researchers have not chosen this method to fabricate thin-film because of the difficulty of controlling the particle size and its surface morphology. The maximum number of reports concerned about the deposition method deals with the thin-film solar cell's structural and electrical properties like CTS. Atomic distribution is their primary focus for CTS compound [45, 48, 50]. The report of Chen et al. (1998) has been described as the process of distribution of Sn and Cu atoms in the tetragonal phase. The report indicates why CTS behaves as a semiconductor, and it has a small number of S-S bonds. Also, Onodo et al. (2000) report shows the distribution of atom in CTS monoclinic compound and also found that it's structure is similar to CuGeS₂. Nomura et al. (2013) reduce the bandgap of CTS thin-film 0.8eV to 0.6eV by Se doping. So it clearly indicates that bandgap of CTS can be reduced by doping[48]. Tiwari et al. (2014) used a solid action-reaction method to determine various sulphur effects on CTS solar cell. Among all of the materials only thiourea was capable to produce CTS compound. Thiourea can be attributed future synthesis of CTS [45].

3) Electrodeposition

The sulphurization of the electron deposition process was described for the fabrication of CTS thin-film solar cell by Berg et al.(2012). The CTS monoclinic phase was found from the EDX and XRD investigation, as well as the direct bandgap, was found as 0.93 eV in the bought EQE and PL method. The CTS's final device was getting the value of Voc of 104 mV, Jsc of 17.0 mA/cm² and FF of 30.4%, and the efficiency of 0.54%. Therefore, the shunt resistance was measured as 23 Ωcm², and the gain EQE was less than 60%. The lower open-circuit voltage was recognized to the lower

shunt resistance of the CTS thin film that was the result of the low efficiency [16].

The electrodeposition method was used for CTS monochromatic phase fabrication as well as used Raman analysis by a similar group [16]. The sample preparation was used for necessary electrodepositing metals and sulphurized them at 550 °C for 2 hr in 500 mbar gas pressure inside a tube furnace.

The highest efficiency of CTS solar cell was achieved in the electrodeposition method by Kokie et al.,(2012). This study analyzed the effect of deposition ratio Cu/Sn on the film properties and varied deposition time to obtain the different ratio of Cu/Sn and the photovoltaic device performance. Therefore, the CTS device was fabricated structure SLG/Mo/CdS/ZnO:Al/Al and the getting efficiency was 2.84% at the ratio Cu/Sn <=2 highest efficiency regarding this electrodeposition method[15].

4) Chemical Bath Deposition

The first CTS thin film was fabricated using the chemical bath deposition technique by Nair et al. (CBD). The sequentially deposited of the CuS and SnS layer using CBD and followed by the annealing in the nitrogen environment for the fabrication of CTS thin film. The CuS layer converted into Cu₂S at the annealing temperature of 300-340 °C, which was reacted with the SnS at the 400 °C. The estimated direct optical band gap was approximately 1eV, whereas, in the dark condition, the electrical conductivity was measured as 1 Ω⁻¹cm⁻¹.

The chemical Bath Deposition processes were studied for the CTS thin film fabrication by the Avellaneda et al. (2010). Hence, the sequential deposition of a 200 nm thick layer of CuS and a 180 nm thick layer of SnS have used CBD method. They were annealed at the 350 °C and 400 °C in a nitrogen environment for the film deposition procedure. The fabrication of CTS as Cu₂SnS₃ and Cu₄SnS₄ was confirmed using the analysis of grazing X-ray diffraction analysis. The optical band gap was measured by transmittance for Cu₄SnS₄ and Cu₂SnS₃ as 1.2 e V and 0.95 eV, respectively. The CTS thin film was shown as a p-type were as the carrier concentration of 10¹⁷-10¹⁸ cm⁻³ and the electrical conductivities of 0.5-10 Ω⁻¹ cm⁻¹. The generated light's current density was calculated 34 mA/cm² and 27 mA/cm² for the Cu₂SnS₃ and Cu₄SnS₄, respectively, in the film's 500 nm thickness. The electron-hole pair was 32% and 24% for the Cu₂SnS₃ and Cu₄SnS₄, converting from the optical conversion of solar energy. The buffer layer was used as CdS and CdS/Cu₂SnS₃ and CdS/Cu₄SnS₄ interface were measured as 0.9 eV and 1.1 eV, respectively [24].

IV. CONCLUSION

In conclusion, the CTS solar cell history was reviewed and discussed as the first successful CTS device to present updated research work regarding the field. Therefore, to understand the properties of the material were discussed the characterization of CTS thin film device. To increase the PCE, different Vacuum deposition techniques and Non – vacuum deposition technique are analyzed. The efficiency, FF, short circuit current, and open-circuit voltage were considered for this study. The details were discussed here like compositional, crystal structural, optical, morphological, and electrical properties. The

fundamental properties structure of CTS thin film solar cell was described. The different methodology is also described briefly here for the fabrication of CTS thin-film solar cells. The highest efficiency of the CTS doping with Ge was getting 6.7% [18]. But theoretically, it will forecast 30%. So, the further enhance the CTS performance needs more investigation in the CTS absorber layer to fulfil the future energy demand.

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